



Haratsch 1-4

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Patent Application

Applicant(s): Haratsch et al.
Case: 1-4
Serial No.: 09/326,785
Patent No.: 6,690,754 *BI*
Issue Date: February 10, 2004
Group: 2631
Examiner: Pankaj Kumar

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *M. J. Coyle* Date: August 30, 2005

Title: Method and Apparatus for Reducing the Computational Complexity and Relaxing the Critical Path of Reduced State Sequence Estimation (RSSE) Techniques

TRANSMITTAL LETTER

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
SEP 08 2005
of Correction

Attn: Certificate of Corrections Branch

Sir:

Submitted herewith are the following documents relating to the above-identified patent:

1. Request for Certificate of Correction Under 37 C.F.R. §1.322(a); and
2. Certificate of Correction.

There is no additional fee due in conjunction with this Certificate of Correction. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error(s). A duplicate copy of this letter is enclosed.

Respectfully,

Kevin M. Mason

Date: August 30, 2005

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Reg. No. 36,597
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Fairfield, CT 06824
(203) 255-6560

SEP 08 2005

Patent Application

Applicant(s): Haratsch et al.
Case: 1-4
Serial No.: 09/326,785
Patent No.: 6,690,754 *B1*
Issue Date: February 10, 2004
Group: 2631
Examiner: Pankaj Kumar

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *M. J. Lane* Date: August 30, 2005

Title: Method and Apparatus for Reducing the Computational Complexity and Relaxing the Critical Path of Reduced State Sequence Estimation (RSSE) Techniques

REQUEST FOR CERTIFICATE OF CORRECTION OF
PATENT FOR PTO MISTAKE(S) UNDER 37 C.F.R. 1.322(a)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.322, the patentee for the above-identified patent hereby requests the issuance of a Certificate of Correction with respect to errors in the above-identified patent.

The errors are set forth on the proposed Certificate of Correction (PTO-1050) submitted herewith, in duplicate.

There is no additional fee due in conjunction with this Certificate of Correction. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. Duplicate copies of this letter are enclosed.

Respectfully,

Kevin M. Mason

Kevin M. Mason
Attorney for Applicant(s)
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Date: August 30, 2005

SEP 08 2005

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,690,754 **B1**
DATED : February 10, 2004
INVENTOR(S) : Haratsch et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the References Cited, change the final "Other Publications" reference to read:

-- W. Lee and F. Hill, "A Maximum-Likelihood Sequence Estimator with Decision-Feedback Equalization", IEEE Transactions on Communications, vol. 25, Issue 9, 971 - 979 (Sep. 1977). --.

In the specification, column 2, line 60, "tentativnre" should be changed to read as:

-- tentative --.

In the specification, column 3, line 40 should be changed to read as:

-- has a length, L, denoted by $\{f_k\}_{k=0}^L$, where f_k is the --.

In the specification, column 4, line 17 should be changed to read as:

-- interference introduced by the tail channel taps $\{f_k\}_{k=U+1}^L$, --.

In the specification, column 4, line 19 should be changed to read as:

-- interference introduced by only the initial channel taps $\{f_k\}_{k=1}^U$ --.

In the specification, column 5, line 38 should be changed to read as:

-- expressed as $\rho_n = (\sigma_{n-K}; X_{n-K}^{m_K}, \dots, X_{n-1}^{m_1})$ where $X_{n-i}^{m_i}$ --.

MAILING ADDRESS OF SENDER:

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PATENT NO. 6,690,754

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,690,754 *Bl*
DATED : February 10, 2004
INVENTOR(S) : Haratsch et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the specification, column 5, lines 44-45 should be changed to read as:

-- The reduced trellis has $S' = S \times 2^{m_1 + \dots + m_k}$ states. --.

In the specification, column 5, line 62, before "previously" and after "the" replace "k" with -- K --.

In the specification, column 6, line 18 should be changed to read as:

-- $b = 2^{m'}$ extensions, then the resulting bM paths are tested for --.

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,690,754 **B1**
DATED : February 10, 2004
INVENTOR(S) : Haratsch et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 1, line 3, "L+ taps" should be changed to read as:
-- L+1 taps --.

In claim 11, line 3, "L+ taps" should be changed to read as:
-- L+1 taps --.

In claim 20, line 3, "L+ taps" should be changed to read as:
-- L+1 taps --.

In claim 21, line 3, "L+ taps" should be changed to read as:
-- L+1 taps --.

In claim 22, line 2, "L+ taps" should be changed to read as:
-- L+1 taps --.

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